

Notice of References Cited	Application/Control No. 09/595,776	Applicant(s)/Patent Under Reexamination MUSOLL ET AL.	
	Examiner David J. Huisman	Art Unit 2183	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,933,627	08-1999	Parady, Bodo	712/228
	B	US-5,758,142	05-1998	McFarling et al.	712/239
	C	US-6,016,542	01-2000	Gottlieb et al.	712/225
	D	US-5,361,337	11-1994	Okin, Kenneth	712/228
	E	US-6,308,261	10-2001	Morris et al.	712/219
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Yoaz et al., Speculation Techniques for Improving Load Related Instruction Scheduling, 1999, pages 42-53
	V	Kessler, R.E., The Alpha 21264 Microprocessor: Out-of-Order Execution at 600 Mhz, August 1998, pages 18-19
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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